



Printed Circuit Board Design Tester - Intern

Internship title: Printed Circuit Board Design Tester - Intern

Organization: ASU SolarSPELL Initiative (Solar Powered Educational Learning Library)

Location: Tempe campus, ISTBX (formerly Wrigley Hall)

Type of internship: For credit or non-paid; approximately 10 hours per week

Application deadline: November 12, 2023; applications will continue to be accepted and reviewed every week until the position is filled.

Start date: January 8, 2024

Description:

The ASU SolarSPELL Initiative is seeking an intern to test newly manufactured printed circuit boards designed by SolarSPELL interns and staff. In particular, the SolarSPELL team is seeking an electrical engineering student who has taken a PCB design class or has equivalent experience.

SolarSPELL is a global educational initiative at Arizona State University that combines solar-powered technology, digital libraries, and local capacity building. We work to build internet-ready skills and improve educational opportunities around the world. Our offline digital library is designed to bring educational content to resource-constrained locations that may lack electricity, internet connectivity, and/or traditional libraries. The SolarSPELL library emits an offline WiFi hotspot, to which any WiFi-capable device (smartphones, tablets, laptops) can connect and freely surf thousands of resources that are carefully curated and continually improved to meet local information needs.

More information can be found here: <http://solarspell.org>.

This internship offers an exciting opportunity to contribute to improving the quality of educational information available to students and teachers worldwide, whose schools

may lack electricity and/or access to the Internet. Your contributions will be brought to the field and used by teachers and students, within months of your internship!

Interns will be expected to attend a new intern orientation upon the start of the internship. Ideal candidates will be detail-oriented and self-starters. Carry out Inspection and testing of electronic sub-assemblies in accordance with test procedures and SolarSPELL standards. Carry out the operation of Automatic Test Equipment including any manual intervention as required by the test specification. Carry out basic fault finding on failed sub-assemblies.

Essential Duties:

- Work with SolarSPELL's tech advisor to improve the testing plan for the newly manufactured printed circuit boards
- Conduct PCB testing
- Upload embedded firmware
- Test embedded firmware to ensure upload was correctly installed
- Make recommendations on alterations for an updated PCB design

Minimal Qualifications:

- PCB design experience (through junior-level PCB design course or equivalent experience)
- Electrical engineering student
- Demonstrated ability to work independently
- Globally minded; you have a desire to work for an organization that serves communities from all over the world
- Available to come to ISTBX on the Tempe campus during normal business hours to conduct testing

How to apply: If you are interested in applying for this position, please submit a resume and cover letter to Cassie Barrett at cbarre38@asu.edu. All documents should be in PDF format and follow the naming style of [LastName_FirstName_DesiredPosition]