



Internship title: Printed Circuit Board Design Tester

Organization: SolarSPELL (Solar Powered Educational Learning Library)

Location: Tempe campus, ISTBX (formerly Wrigley Hall)

Type of internship: Non-paid; approximately 10 hours per week

Application deadline: July 31, 2023; applications will continue to be accepted and reviewed every week until the position is filled.

Start date: Aug 15, 2023

Description:

SolarSPELL is seeking an intern to test new manufactured printed circuit boards designed by SolarSPELL interns and staff. In particular, the SolarSPELL team is seeking an electrical engineer who has taken a PCB design class or has equivalent experience.

SolarSPELL at ASU is a global educational initiative that combines curated digital libraries, solar-powered technology, and the training to build information literacy and internet-ready skills in offline environments, focusing on the half of the world that remains unconnected. Our offline digital library is designed to bring educational content to resource-constrained locations that may lack electricity, Internet connectivity, and/or traditional libraries. The SolarSPELL library emits an offline WiFi hotspot, to which any WiFi capable device (smartphones, tablets, laptops) can connect and browse the expansive content for free.

More information on SolarSPELL can be found here: <http://solarspell.org>

This internship offers an exciting opportunity to contribute to improving the quality of educational information available to students and teachers around the world, whose schools may lack Internet connectivity, electricity, and/or traditional libraries. Your contributions will be brought to the field and used by teachers and students, within months of your internship!

Interns will be expected to attend a new intern orientation upon the start of the internship. Ideal candidates will be detail-oriented and self-starters.

Essential Duties:

- Work with SolarSPELL's tech advisor to develop a testing plan for the newly manufactured printed circuit boards
- Conduct PCB testing
- upload embedded firmware
- Test embedded firmware to ensure upload was correctly installed
- Make recommendations on alterations for an updated PCB design

Minimal Qualifications:

- PCB design experience (through junior-level PCB design course or equivalent experience)
- Electrical engineer
- Available to come to Tempe campus during normal business hours to conduct testing

How to apply: If you are interested in applying for this position, please submit a resume and cover letter to Rachel Nova at rnova@asu.edu. All documents should be in PDF format and follow the naming style of [LastName_FirstName_DesiredPosition]